

Form PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR INFORMATION DISCLOSURE STATEMENT  
(Use Several Sheets if Necessary)

APPLICANT: David T. Blaauw et al.

ATTY. DOCKET #: SC11341TC

FILING DATE: Concurrently Herewith

APPL. #: Unknown

GROUP: Unknown

1011 U.S. PTO  
09/858126



### U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AA		6,069,521	5/30/00	Taylor et al.	327	540	6/27/97
AB		6,028,417	2/22/00	Ang et al.	323	209	6/27/97
AC							
AD							
AE							
AF							
AG							
AH							
AI							
AJ							
AK							

### FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE (#43)	COUNTRY	CLASS	SUBCLASS
AL						
AM						
AN						
AO						
AP						

### OTHER INFORMATION (Including Author, Title, Date, Pertinent Pages, Etc.)

AR	Pant et al., "An Architectural Solution for the Inductive noise Problem due to Clock-Gating," ISLPED99, San Diego, CA, pgs. 255-257 (1999).
AS	Pant et al., "Inductive Noise Reduction at the Architectural Level," Georgia Institute of Technology, Intel Corporation, 6 pgs.
AT	Ang, "An On-chip Voltage Regulator using Switched Decoupling Capacitors," IEEE International Solid-State Circuits Conference, pg. 438-439 (2000).
AU	Heald, "A Third-Generation SPRC V9 64-b Microprocessor," IEEE, pgs. 1526-2000 (2000).
AV	
AW	
AX	
AY	
AZ	

EXAMINER *H. Jackson* DATE CONSIDERED *11-3-03*

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.